EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	12	(load$3 with (perform$3 execut$3) with background) same thread	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L3	384	(load$3 near5 inactive) and g06f$.ipc.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L4	33	(load$3 near5 inactive near4 register) and g06f$.ipc.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L5	115	"row address registers"	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L6	69	"row address registers" and DRAM	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L7	53	inagami.in.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L8	2	inagami.in. and mask	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L9	7	(instruction command) with (deactivate de-activate) with row with DRAM	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L10	163	(instruction command) with (deactivate de-activate) with (row DRAM)	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L11	19	(instruction command) with (deactivate de-activate) with DRAM	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L12	1	"5933627".pn.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L13	43	((functional arithmetic) adj unit) with generat$3 near3 "memory address"	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L14	1	"5,896,523".pn.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L15	43	dowling-e$.in. and embedded	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L16	0	(US-20020091916-$).did.	US-PGPUB	OR	OFF	2007/01/17 15:37
L17	0	L16 and pointer with file	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L18	74	"register file" with act$3 with cache	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L19	62	712/5.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L20	157	712/224.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L21	499	712/207.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37

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L22	217	712/235.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L23	292	712/237.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L24	612	712/225.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L25	644	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2007/01/17 15:37
L26	0	dowling-eric.in. and g06f$.ipc.	US-PGPUB	OR	OFF	2007/01/17 15:37
L27	0	dowling-eric.in.	US-PGPUB	OR	OFF	2007/01/17 15:37
L28	50	dowling-e$.in.	US-PGPUB	OR	OFF	2007/01/17 15:37
L29	1	(US-20020091916-$).did.	US-PGPUB	OR	OFF	2007/01/17 15:37
L30	1	L29 and (row with address with register same load$3 with parallel$2)	US-PGPUB	OR	ON	2007/01/17 15:37
L31	0	L29 and (DRAM same recharg$3)	US-PGPUB	OR	ON	2007/01/17 15:37
L32	2775	"active memory"	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L33	0	"active memory" same DRAM same processor same embedded	US-PGPUB; USPAT	OR	ON	2007/01/17 15:40
L34	24	"active memory" and DRAM same processor same embedded	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L35	454	DRAM with processor with embedded	US-PGPUB; USPAT	OR	ON	2007/01/17 15:37
L36	152	DRAM with processor with embedded with (chip board)	US-PGPUB; USPAT	OR	ON	2007/01/17 15:40
L37	12	DRAM with processor with embedded with (chip board)	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 15:41
L38	0	"active memory" same DRAM same processor same embedded	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/17 15:40
S1	5	(load$3 with (perform$3 execut$3) with background) same thread	US-PGPUB; USPAT	OR	OFF	2004/12/01 14:16
S2	299	(load$3 near5 inactive) and g06f$.ipc.	US-PGPUB; USPAT	OR	OFF	2004/12/01 14:17
S3	32	(load$3 near5 inactive near4 register) and g06f$.ipc.	US-PGPUB; USPAT	OR	OFF	2004/12/01 14:27

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S4	84	"row address registers"	US-PGPUB; USPAT	OR	OFF	2004/12/01 14:27
S5	54	"row address registers" and DRAM	US-PGPUB; USPAT	OR	OFF	2004/12/01 14:32
S6	53	inagami.in.	US-PGPUB; USPAT	OR	OFF	2004/12/01 14:32
S7	2	inagami.in. and mask	US-PGPUB; USPAT	OR	OFF	2004/12/02 08:08
S8	6	(instruction command) with (deactivate de-activate) with row with DRAM	US-PGPUB; USPAT	OR	ON	2004/12/02 08:09
S9	116	(instruction command) with (deactivate de-activate) with (row DRAM)	US-PGPUB; USPAT	OR	ON	2004/12/02 08:12
S10	14	(instruction command) with (deactivate de-activate) with DRAM	US-PGPUB; USPAT	OR	ON	2004/12/02 08:12
S11	1	"5933627".pn.	US-PGPUB; USPAT	OR	OFF	2004/12/02 10:26
S12	41	((functional arithmetic) adj unit) with generat$3 near3 "memory address"	US-PGPUB; USPAT	OR	ON	2004/12/02 16:50
S13	1	"5,896,523".pn.	US-PGPUB; USPAT	OR	OFF	2004/12/06 14:01
S14	23	dowling-e$.in. and embedded	US-PGPUB; USPAT	OR	OFF	2004/12/06 14:02
S15	1	(US-20020091916-$).did.	US-PGPUB	OR	OFF	2004/12/06 14:02
S16	1	S15 and pointer with file	US-PGPUB; USPAT	OR	ON	2004/12/06 14:57
S17	43	"register file" with act$3 with cache	US-PGPUB; USPAT	OR	ON	2004/12/06 14:58
S18	46	712/5.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/06 16:19
S19	397	712/207.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/06 16:19
S20	137	712/224.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/06 16:19
S21	467	712/225.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/06 16:19
S22	484	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/06 16:19
S23	166	712/235.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/06 16:19
S24	244	712/237.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 10:16

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S25	0	dowling-eric.in. and g06f$.ipc.	US-PGPUB	OR	OFF	2005/07/20 17:23
S26	0	dowling-eric.in.	US-PGPUB	OR	OFF	2005/07/20 17:23
S27	20	dowling-e$.in.	US-PGPUB	OR	OFF	2005/07/20 17:23
S28	1	(US-20020091916-$).did.	US-PGPUB	OR	OFF	2005/07/20 17:24
S29	1	S28 and (row with address with register same load$3 with parallel$2)	US-PGPUB	OR	ON	2005/07/20 17:25
S30	0	S28 and (DRAM same recharg$3)	US-PGPUB	OR	ON	2005/07/20 17:25
S31	2486	"active memory"	US-PGPUB; USPAT	OR	ON	2006/03/27 15:50
S32	0	"active memory" same DRAM same processor same embedded	US-PGPUB; USPAT	OR	ON	2006/03/27 15:50
S33	23	"active memory" and DRAM same processor same embedded	US-PGPUB; USPAT	OR	ON	2006/03/27 15:52
S34	402	DRAM with processor with embedded	US-PGPUB; USPAT	OR	ON	2006/03/27 15:52
S35	138	DRAM with processor with embedded with (chip board)	US-PGPUB; USPAT	OR	ON	2006/03/27 15:52